

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		ATTY. DOCKET NO. Y0R9-2001-0602 (8728-546)	SERIAL NO. Unassigned
		APPLICANT Michael K. Gschwind, Valentina Salapura	JC997 U.S. PTO 09/940709 08/28/01
		FILING DATE Concurrently herewith	GROUP Art Unit

U.S. PATENT DOCUMENT

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>wlc</i>	1.	U.S. Patent Application Serial No.: 09/649,863 filed August 28, 2000. "Cache Memory System For Selectively Storing Directory Information For a Higher Level Cache In Portions of a Lower Level Cache".
<i>↓</i>	2.	U.S. Provisional Patent Application Serial No.: 60/210,858 filed June 9, 2000. "Cache Memory System that Selectively Stores Directory Information for a Higher Level Cache Data Array in Portions of a Lower Level Cache Data Array"
<i>↓</i>	3.	Montoye et al., "Chipstack and DCA DRAM: Maximize Bandwidth and Size at Reasonable Cost." Solving the Memory Wall Problem Workshop, Vancouver, B.C., June 11, 2000.

EXAMINER *Todd L. Johnson* DATE CONSIDERED *4/29/04*

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.